

Description

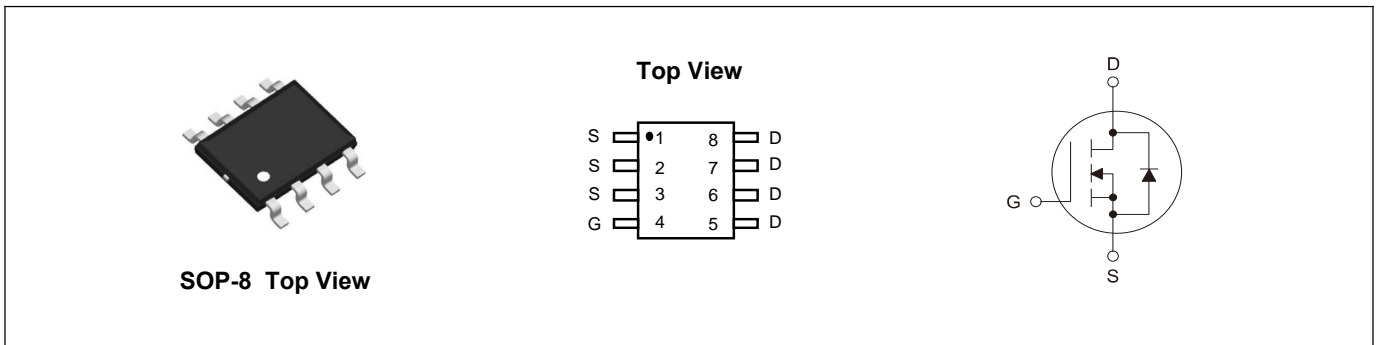
The AT06045S is the high cell density trenched N-ch MOSFETs, which provide excellent $R_{DS(ON)}$ and gate charge for most of the synchronous buck converter applications.

The AT06045S meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Product Summary



V_{DS}	60 V
I_D (at $V_{GS}=10V$)	4.5 A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	40 m Ω
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	50 m Ω



Absolute Maximum Ratings

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current, V_{GS} @ 10V	$I_D@T_A=25^\circ C$	4.5	A
Continuous Drain Current, V_{GS} @ 10V	$I_D@T_A=70^\circ C$	3.5	A
Pulsed Drain Current ²	I_{DM}	18	A
Single Pulse Avalanche Energy ³	EAS	22	mJ
Avalanche Current	I_{AS}	21	A
Total Power Dissipation	$P_D@T_A=25^\circ C$	1.5	W
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ C$
Operating Junction Temperature Range	T_J	-55 to 150	$^\circ C$

Thermal Data

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient	$R_{\theta JA}$	---	85	$^\circ C/W$
Thermal Resistance Junction-Case	$R_{\theta JC}$	---	25	$^\circ C/W$

Electrical Characteristics ($T_J=25\text{ }^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60	---	---	V
BV_{DSS} Temperature Coefficient	BV_{DSS}/T_J	Reference to $25\text{ }^\circ\text{C}$, $I_D=1mA$	---	0.044	---	$V/^\circ\text{C}$
Static Drain-Source On-Resistance ²	$R_{DS(ON)}$	$V_{GS}=10V, I_D=4A$	---	---	40	$m\Omega$
		$V_{GS}=4.5V, I_D=2A$	---	---	50	$m\Omega$
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	---	2.5	V
$V_{GS(th)}$ Temperature Coefficient	$V_{GS(th)}$		---	-4.8	---	$mV/^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=48V, V_{GS}=0V, T_J=25\text{ }^\circ\text{C}$	---	---	1	μA
		$V_{DS}=48V, V_{GS}=0V, T_J=55\text{ }^\circ\text{C}$	---	---	5	
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
Forward Transconductance	g_{fs}	$V_{DS}=5V, I_D=4A$	---	28.3	---	S
Gate Resistance	R_g	$V_{DS}=0V, V_{GS}=0V, f=1MHz$	---	2.5	---	Ω
Total Gate Charge (10V)	Q_g	$V_{DS}=48V, V_{GS}=10V, I_D=4A$	---	19	---	nC
Gate-Source Charge	Q_{gs}		---	2.6	---	
Gate-Drain Charge	Q_{gd}		---	4.1	---	
Turn-On Delay Time	$T_{d(on)}$	$V_{DD}=30V, V_{GS}=10V, R_G=3.3\Omega, I_D=4A$	---	3	---	ns
Rise Time	T_r		---	34	---	
Turn-Off Delay Time	$T_{d(off)}$		---	23	---	
Fall Time	T_f		---	6	---	
Input Capacitance	C_{iss}	$V_{DS}=15V, V_{GS}=0V, f=1MHz$	---	1027	---	pF
Output Capacitance	C_{oss}		---	65	---	
Reverse Transfer Capacitance	C_{rss}		---	46	---	

Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current ^{1,5}	I_S	$V_G=V_D=0V$, Force Current	---	---	4.5	A
Pulsed Source Current	I_{SM}		---	---	18	A
Diode Forward Voltage ²	V_{SD}	$V_{GS}=0V, I_S=1A, T_J=25\text{ }^\circ\text{C}$	---	---	1.2	V
Reverse Recovery Time	t_{rr}	$I_F=4A, di/dt=100A/\mu s, T_J=25\text{ }^\circ\text{C}$	---	12.1	---	nS
Reverse Recovery Charge	Q_{rr}		---	6.7	---	nC

Note :

- The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating. The test condition is $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=21A$
- The power dissipation is limited by $150\text{ }^\circ\text{C}$ junction temperature
- The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

Typical Characteristics

