

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

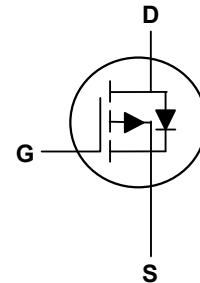
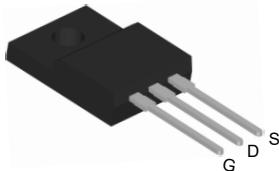
## Product Summary



$V_{DS}$	-60	V
$I_D$	-45	A
$R_{DS(ON)}$ (at $V_{GS}=-10V$ )	24	mΩ
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	35	mΩ

## Applications

- High Frequency Point-of-Load,Synchronous Buck Converter
- Networking DC-DC Power System
- Power Tool Application



TO-220F Top View

## Absolute Maximum Ratings( $T_c=25^\circ C$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D @ T_c = 25^\circ C$	-45	A
Continuous Drain Current <sup>1</sup>	$I_D @ T_c = 100^\circ C$	-34	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	-90	A
Single Pulse Avalanche Energy <sup>3</sup>	EAS	101	mJ
Avalanche Current	$I_{AS}$	-45	A
Total Power Dissipation <sup>4</sup>	$P_D @ T_c = 25^\circ C$	60	W
Storage Temperature Range	$T_{STG}$	-55 to 150	°C
Operating Junction Temperature Range	$T_J$	-55 to 150	°C

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	62	°C/W
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	4.2	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-60	---	---	V
Static Drain-Source On-Resistance <sup>2</sup>	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}$ , $I_D=-18\text{A}$	---	20	24	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$ , $I_D=-12\text{A}$	---	29	35	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = -250\mu\text{A}$	-1.0	---	-2.5	V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	1	$\text{uA}$
		$V_{\text{DS}}=-48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=55^\circ\text{C}$	---	---	5	$\text{uA}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
Forward Transconductance	$g_{\text{fs}}$	$V_{\text{DS}}=-10\text{V}$ , $I_D=-18\text{A}$	---	23	---	S
Gate Resistance	$R_g$	$V_{\text{DS}}=0\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	7	14	$\Omega$
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-20\text{V}$ , $V_{\text{GS}}=-4.5\text{V}$ , $I_D=12\text{A}$	---	25	---	nC
Gate-Source Charge	$Q_{\text{gs}}$		---	6.7	---	
Gate-Drain Charge	$Q_{\text{gd}}$		---	5.5	---	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DD}}=-15\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $R_G=3.3\Omega$ , $I_D=-1\text{A}$	---	38	---	ns
Rise Time	$T_r$		---	23.6	---	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	100	---	
Fall Time	$T_f$		---	6.8	---	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-15\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	3235	---	pF
Output Capacitance	$C_{\text{oss}}$		---	210	---	
Reverse Transfer Capacitance	$C_{\text{rss}}$		---	120	---	

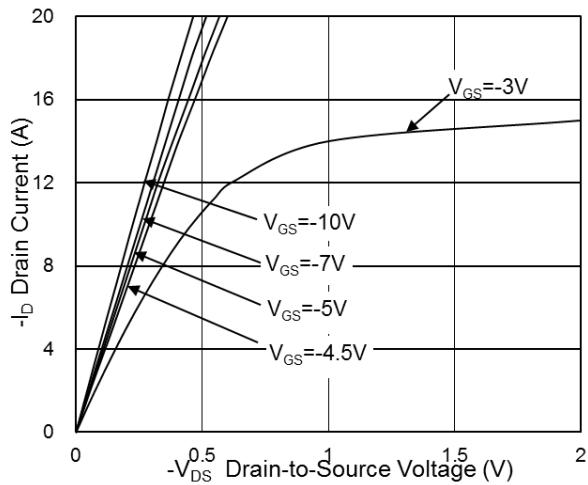
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current <sup>1,5</sup>	$I_s$	$V_G=V_D=0\text{V}$ , Force Current	---	---	-20	A
Diode Forward Voltage <sup>2</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}$ , $I_s=-1\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1	V

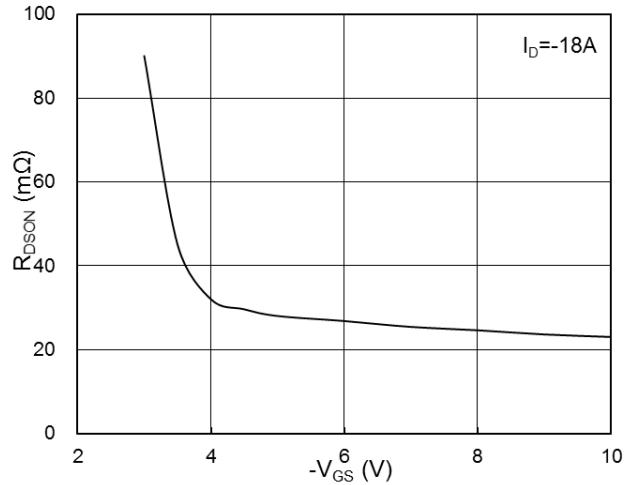
**Note:**

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=-25\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $L=0.1\text{mH}$ , $I_{\text{AS}}=-45\text{A}$
- 4.The power dissipation is limited by  $150^\circ\text{C}$  junction temperature
- 5.The data is theoretically the same as  $I_D$  and  $I_{\text{DM}}$  , in real applications , should be limited by total power dissipation.

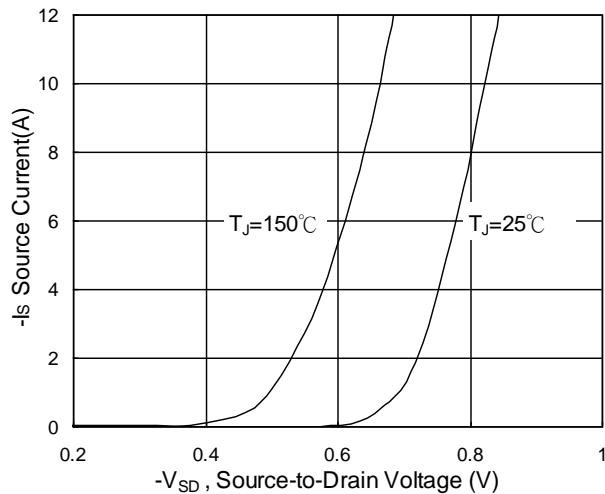
## Typical Characteristics



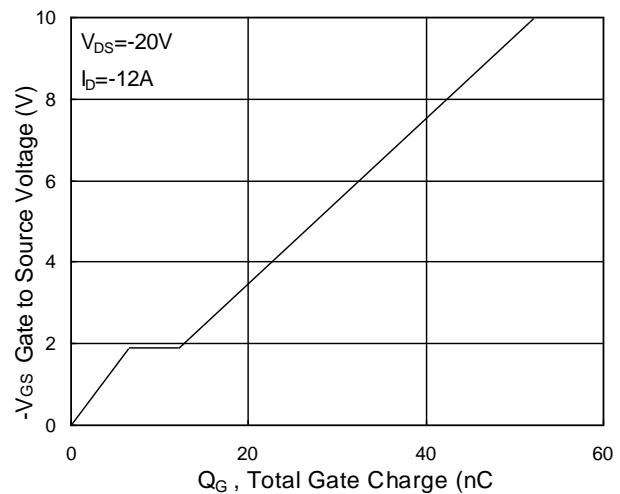
**Fig.1 Typical Output Characteristics**



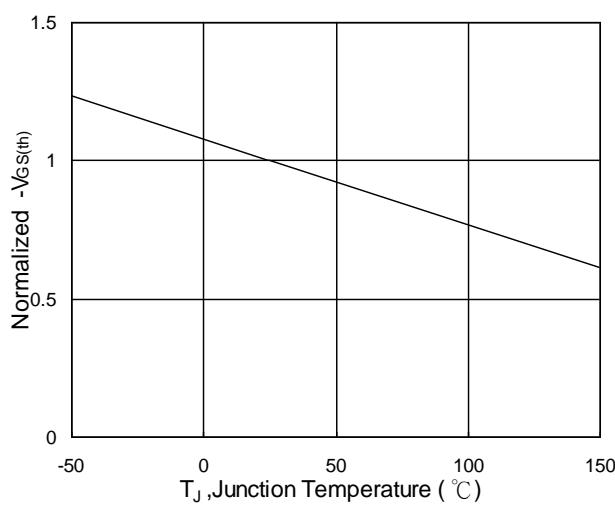
**Fig.2 On-Resistance vs. G-S Voltage**



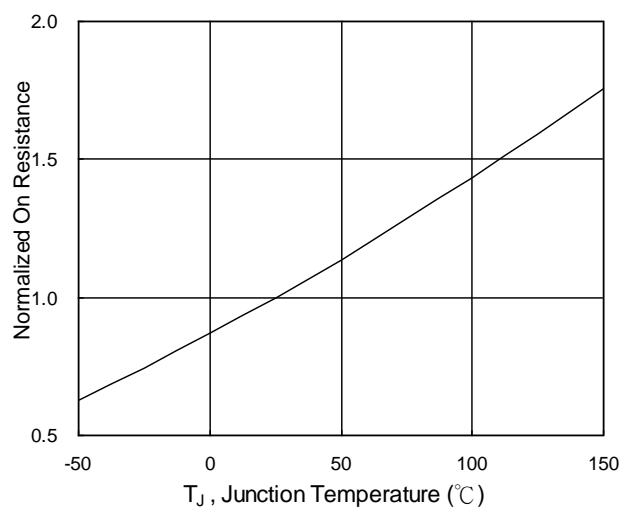
**Fig.3 Source Drain Forward Characteristics**



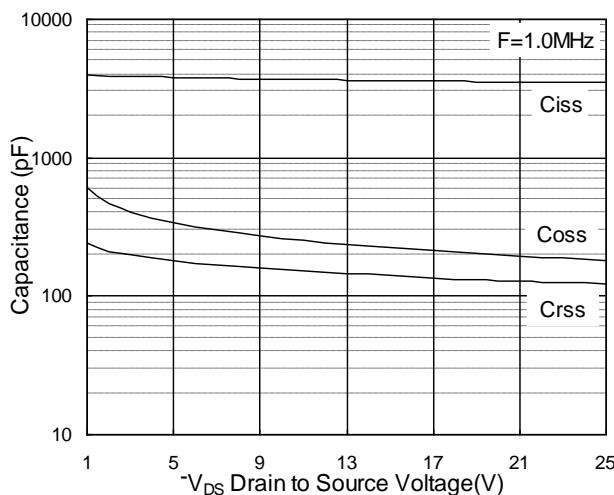
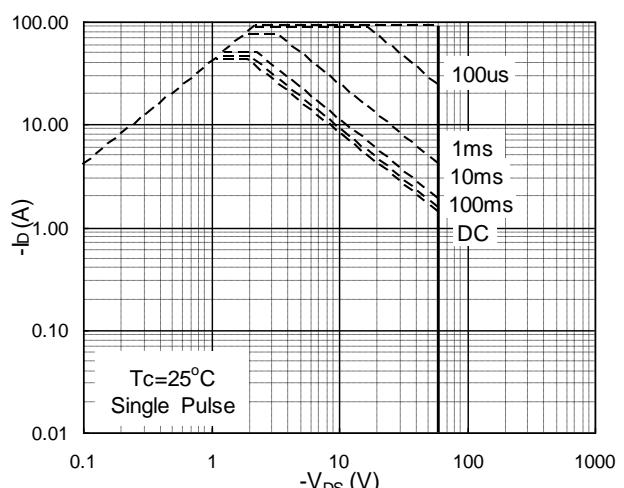
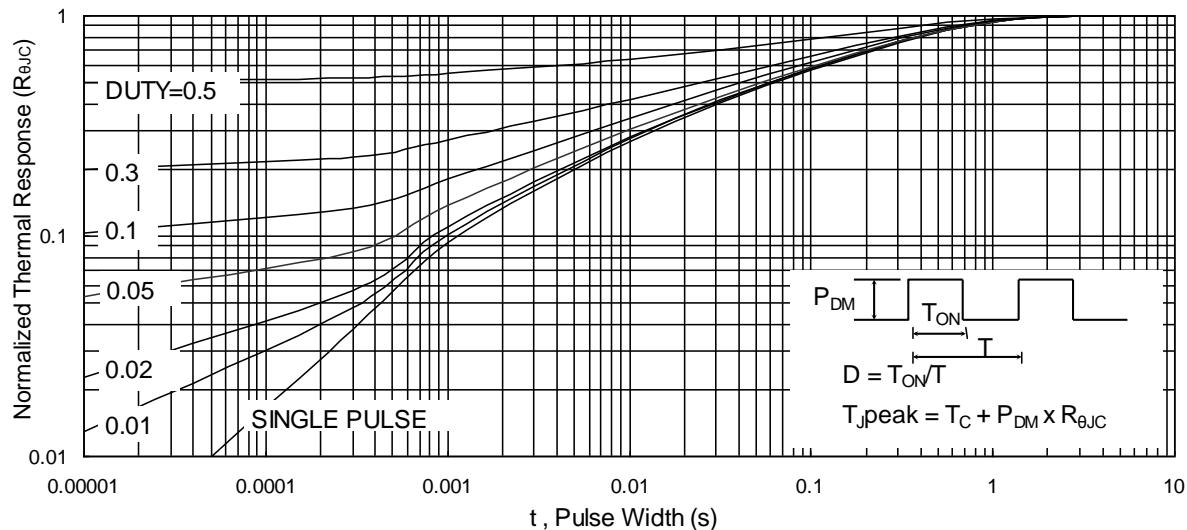
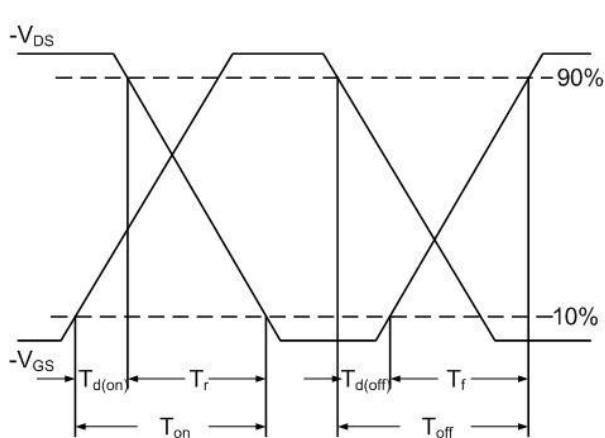
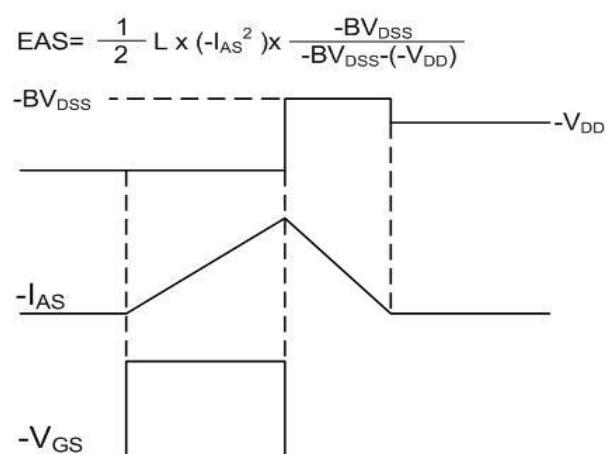
**Fig.4 Gate-Charge Characteristics**



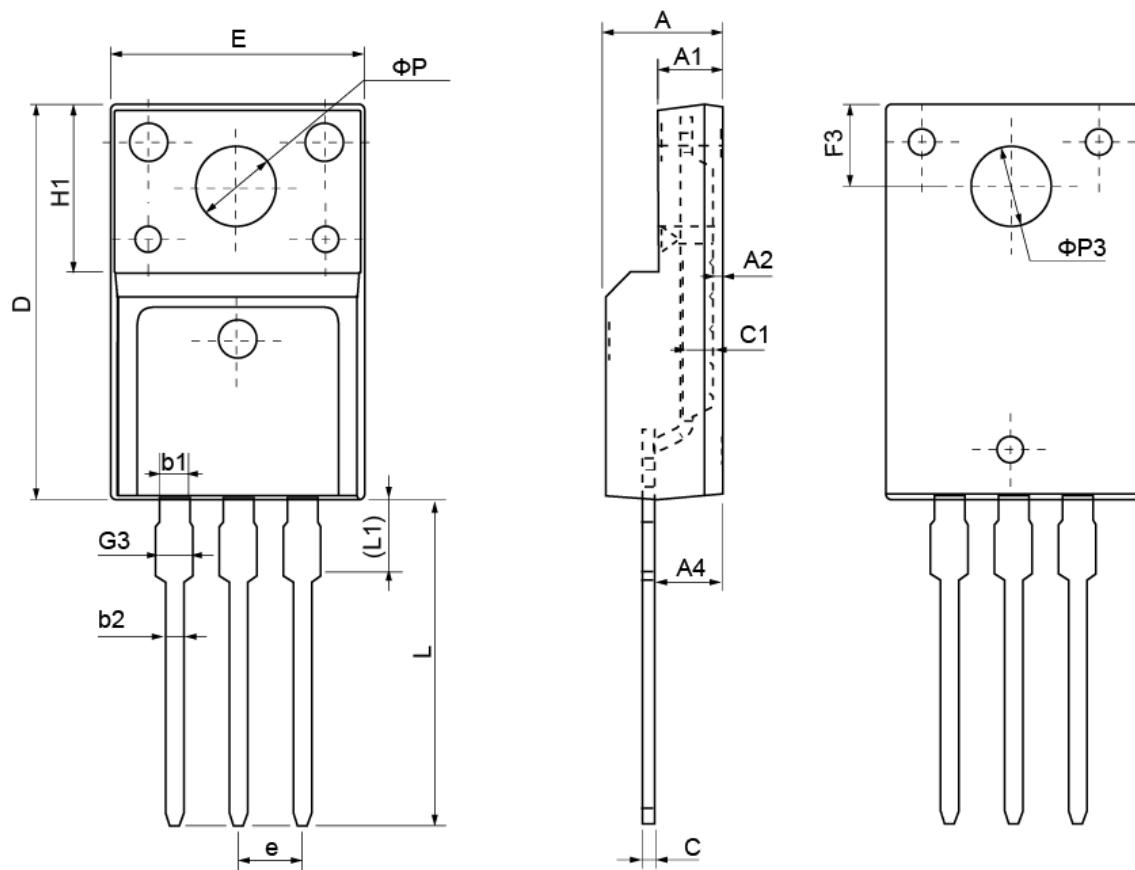
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**


**Fig.7 Capacitance**

**Fig.8 Safe Operating Area**

**Fig.9 Normalized Maximum Transient Thermal Impedance**

**Fig.10 Switching Time Waveform**

**Fig.11 Unclamped Inductive Waveform**

### TO-220F Package Outline Dimensions



<b>Symbol</b>	<b>Dimensions (unit:mm)</b>			<b>Symbol</b>	<b>Dimensions (unit:mm)</b>		
	<b>Min</b>	<b>Typ</b>	<b>Max</b>		<b>Min</b>	<b>Typ</b>	<b>Max</b>
<b>A</b>	4.40	4.70	5.00	<b>H1</b>	6.70 REF		
<b>A1</b>	2.30	2.55	2.80	<b>L</b>	12.30	12.98	13.30
<b>A2</b>	0.30	0.50	0.70	<b>L1</b>	2.95	3.10	3.50
<b>A4</b>	2.45	2.80	3.05	<b>φ P</b>	3.03	3.20	3.50
<b>c</b>	0.30	0.50	0.70	<b>φ P3</b>	3.15	3.45	3.65
<b>c1</b>	1.20	1.30	1.40	<b>b1</b>	1.10	1.30	1.45
<b>D</b>	15.40	15.90	16.40	<b>b2</b>	0.60	0.80	1.00
<b>E</b>	9.86	10.16	10.46	<b>F3</b>	3.05	3.30	3.55
<b>e</b>	2.54 BSC			<b>G3</b>	1.15	1.30	1.40